



1374.39158CX1

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicants: Norio HASEGAWA, et al.
Serial No.: 10/770,413
Filed: February 4, 2004
Title: METHOD OF MANUFACTURING SEMICONDUCTOR
INTEGRATED CIRCUIT DEVICES
Group: 1756
Examiner: CHACKO DAVIS, Daborah
Confirmation No.: 1925

AMENDMENT

Mail Stop: AMENDMENT

Commissioner for Patents

P.O. Box 1450

Alexandria, Virginia 22313-1450

June 21, 2006

Sir:

In response to the Office Action dated December 27, 2005, the period of response for which extension of time is requested in the attached Petition for Extension of Time, please amend the above-identified application as listed below and as set forth on the following pages:

Amendments to the claims begin on page 2;

Remarks are included beginning on page 7; and

Authorization is included on page 13.